

## **AMENDMENTS TO THE CLAIMS**

The following listing of claims will replace all prior versions and listings of claims in the application.

### **LISTING OF CLAIMS**

1. (Currently Amended) A semiconductor device comprising:

a first semiconductor package having a first semiconductor chip;

a second semiconductor package supported on the first semiconductor package so that an end of the second semiconductor package is arranged directly above the first semiconductor chip; and

a third semiconductor package independent from said second semiconductor package, said third semiconductor package supported on the first semiconductor package so that an end of the third semiconductor package is arranged directly above the first semiconductor chip;

a first projection supporting the end of the second semiconductor package directly above the first semiconductor chip; and

a second projection supporting the end of the third semiconductor package directly above the first semiconductor chip.

2. (Cancelled)

3. (Currently Amended) The semiconductor device according to Claim [[2]]  
1, wherein the second semiconductor package is spaced apart from the third semiconductor package.

4. (Currently Amended) The semiconductor device according to Claim [[2]]  
1, wherein the second semiconductor package and the third semiconductor package  
are different in at least one of size, thickness, and material.

5. (Currently Amended) The semiconductor device according to Claim [[2]]  
1, wherein at least one of a space between the second semiconductor package and the  
third semiconductor package, a space between the first semiconductor package and the  
second semiconductor package, and a space between the first semiconductor package  
and the third semiconductor package is filled with resin.

6. (Original) The semiconductor device according to Claim 1, wherein the  
first semiconductor package has a first carrier substrate, the first semiconductor chip  
being flip-chip mounted on or above the first carrier substrate; and

the second semiconductor package has second semiconductor chips mounted  
on or above a second carrier substrate, a bump that is bonded to the first carrier  
substrate and that holds the second carrier substrate on or above the first  
semiconductor chip, and a seal for sealing the second semiconductor chips.

7. (Original) The semiconductor device according to Claim 6, wherein the  
first semiconductor package comprises a ball rid array package in which the first  
semiconductor chip is flip-chip mounted on or above the first carrier substrate; and the  
second semiconductor package comprises at least one of a ball grid array package and

a chip-size package in which the second semiconductor chips mounted on or above the second carrier substrate are sealed by molding.

8. (Original) The semiconductor device according to Claim 6, wherein the bump is arranged on the second carrier substrate away from the mounting region of the first semiconductor chip; and

the projection is arranged so that the second carrier substrate is supported at four corners.

9. (Original) The semiconductor device according to Claim 6, wherein the first semiconductor chip comprises a logical operation element; and  
the second semiconductor chips comprise memory elements.

10. (Original) The semiconductor device according to Claim 6, wherein the second semiconductor chips have a three-dimensionally mounted structure.

11. (Currently Amended) An electronic device comprising:  
a first package having an electronic component;  
a second package supported on the first package so that an end of the second package is arranged directly above the electronic component; and  
a third package that is independent from the second package, said third package is supported on the first package so that an end of the third package is arranged directly above the electronic component;

a first projection supporting the end of the second package directly above the electronic component; and

a second projection supporting the end of the third package directly above the electronic component.

12. (Currently Amended) An electronic apparatus comprising:

a first semiconductor package having a semiconductor chip;  
a second semiconductor package supported on the first semiconductor package so that an end of the second semiconductor package is arranged directly above the semiconductor chip;

a third semiconductor package that is independent of said second semiconductor package, said third semiconductor package is supported on the first semiconductor package so that an end of the third semiconductor package is arranged directly above the semiconductor chip;

a first projection supporting the end of the second semiconductor package directly above the semiconductor chip; and

a second projection supporting the end of the third semiconductor package directly above the semiconductor chip; and

a motherboard having the second semiconductor package and the third semiconductor package.

13. (Currently Amended) A method for manufacturing a semiconductor device comprising the steps of:

mounting a first semiconductor package including a first semiconductor chip on or above a first carrier substrate;

mounting a second semiconductor package including a plurality of second semiconductor chips on or above a second carrier substrate;

forming a first bump on ~~the~~ an underside of the second carrier substrate away from areas surrounding at least one vertex of the second carrier substrate;

forming a first projection on areas surrounding the other vertices displaced from the first bump; and

bonding the first bump to the first carrier substrate so that the first projection is arranged on the first semiconductor chip;

mounting a third semiconductor package including a plurality of third semiconductor chips on or above a third carrier substrate, the third semiconductor package is independent from the second semiconductor package;

forming a second bump on an underside of the third carrier substrate away from areas surrounding at least one vertex of the third carrier substrate;

forming a second projection on areas surrounding other vertices displaced from the second bump; and

bonding the second bump to the second carrier substrate so that the second projection is arranged on the first semiconductor chip.

14. (Cancelled)

15. (Currently Amended) A method for manufacturing an electronic device comprising the steps of:

mounting a first package having a first electronic component on or above a first carrier substrate;

mounting a second package of second electronic components on or above a second carrier substrate;

mounting a third package of third electronic components on or above a third carrier substrate, the third package is independent of the second package;

forming a first bump on ~~the~~ an underside of the second carrier substrate away from areas surrounding at least one vertex of the second carrier substrate;

forming a second bump on an underside of the third carrier substrate away from areas surrounding at least one vertex of the third carrier substrate;

forming a first projection on areas surrounding the other vertices displaced from the first bump; and

forming a second projection on areas surrounding the other vertices displaced from the second bump;

bonding the first bump to the first carrier substrate so that the first projection is arranged on the first electronic component; and

bonding the second bump to the second carrier substrate so that the second projection is arranged on the first electronic component.